

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,067,844 B2
APPLICATION NO. : 09/832844
DATED : June 27, 2006
INVENTOR(S) : Shunpei Yamazaki

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the Title Page, Item (56)
In the "References Cited":

At page 4, lines 26-29, delete the following:

Hatalis et al., "High Performance Thin Film Transistors in Low-Temperature Crystallized LPCVD Amorphous Silicon Films", Aug. 1987, pp. 161-164, IEEE Electron Device Letters, vol. EDL-8, No. 8.

Signed and Sealed this

Seventeenth Day of July, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office